

In the specification

Please amend the title of the patent application, on line 4 of page 1, as follows:

FRAME BUFFER FOR A SERIALLY ADDRESSABLE NON-DMA DISPLAY

Please amend the abstract of the disclosure, on page 22, lines 2-7, as follows:

A system of an embodiment of the invention is disclosed that includes a frame buffer memory for a serially addressable, ~~non-direct memory access (DMA)~~ display. The frame buffer memory has a number of pixels corresponding to a number of pixels of the ~~non-DMA~~ display. The system also includes a display data transfer circuit, to serially transfer the pixels of the frame buffer memory to the ~~non-DMA~~ display to update the ~~non-DMA~~ display.